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CONFIRMATION NO. 7723

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|-----------------------------|---------------------------------------|--------------|------------------------|--|
| SERIAL NUMBER 10/763,626 | FILING DATE 01/23/2004 RULE | CLASS 438 | GROUP ART UNIT 2813 | ATTORNEY DOCKET NO. 856063.729D1 |
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/199,964 07/18/2002 PAT 6,724,009

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 01830486.5 07/19/2001

EUROPEAN PATENT OFFICE (EPO) 01127923.9 11/23/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/29/2004

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|--|----------|---------|--------|-------------|
| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR | SHEETS | TOTAL | INDEPENDENT |
| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | COUNTRY | DRAWING | CLAIMS | CLAIMS |
| Verified and Acknowledged | ITALY | 1 | 20 | 3 |
| Examiner's Signature <i>TN</i> | Initials | | | |

ADDRESS

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SEED INTELLECTUAL PROPERTY LAW GROUP PLLC

701 FIFTH AVE

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SEATTLE, WA

98104-7092

TITLE

Semiconductor integrated electronic device and corresponding manufacturing method

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| FILING FEE | FEES: Authority has been given in Paper | <input type="checkbox"/> All Fees |
| | | <input type="checkbox"/> 1.16 Fees (Filing) |
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